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AFT 2/822\$

Attorney's Docket 044020-0284032
Client Reference: 01F171

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of:
TAKASHI YAMADA ET AL.

Confirmation Number: 8085

Application No.: 09/993,967

Group Art Unit: 2822

Filed: November 27, 2001

Examiner: Lewis, Monica

For: SEMICONDUCTOR DEVICE WITH VERTICAL TRANSISTOR FORMED IN A SILICON-ON-INSULATOR SUBSTRATE

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Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT/RESPONSE TRANSMITTAL

Transmitted herewith is an amendment/response for this application.

EXTENSION OF TIME

The proceedings herein are for a patent application and the provisions of 37 C.F.R. 1.136 apply. Applicant petitions for a 1 month extension of time under 37 C.F.R. 1.136.

FEES

The fee for claims and extension of time (37 C.F.R. 1.16 and 1.17) has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDIT. FEE
TOTAL	14	- 26	= 0	x \$ 18.00	= \$ 0.00
INDEP.	2	- 6	= 0	x \$ 86.00	= \$ 0.00
FIRST PRESENTATION OF MULTIPLE DEP. CLAIM				+ \$ 290.00	= \$ 0.00
TOTAL ADDITIONAL CLAIM FEE					\$ 0.00
EXTENSION OF TIME FEE					\$ 110.00
GRAND TOTAL					\$ 110.00

FEE PAYMENT

Authorization is hereby made to charge the amount of \$110.00 to Deposit Account No. 033975. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above. A duplicate of this paper is attached.

Date: 11/10/03
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